

Beril GÜNAY

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Date of Birth: 06.05.1998 ▪ Nationality: Turkish

Personal Statement

Currently working as an FPGA Design Engineer and pursuing a Master's degree in Computer Engineering. Highly interested in computer graphics and image processing. Seeking to improve skills by working in game development, VR/MR/AR, and computer vision. Highly driven, competent, self-reliant and adaptable individual with a detailed and analytical approach towards the projects worked on.

Experience

- **FPGA Design Engineer, ASELSAN A.Ş., Ankara (Turkey)** *(August 2020 – Present)*
 - Gained experience in DO-254 compliant, safety-critical PLD architecture design with VHDL
 - Used PLD development tools such as Quartus, Libero, HDL Designer, Modelsim
 - Used workflow management and version control tools such as IBM Rational ClearQuest, JIRA, SVN, SAP
 - Developed and managed DO-254 compliant requirements using IBM DOORS
 - Implemented image processing and computer graphics algorithms - scaling, rasterization
 - Implemented low-speed & high-speed interfaces - UART, I2C, Ethernet
 - Used Python for debugging purposes, for feasibility analysis and modeling of PLD design, for code generation, for preprocessing large amounts of mostly visual data, and in general to speed up processes apt for automatization
 - Prepared informative documents to ease and standardize the onboarding process of new team members
- **Intern, ASELSAN A.Ş., Ankara (Turkey)** *(June – July 2019)*

Implemented a custom communications protocol for computer and board communication with C
- **Intern, TÜBİTAK UZAY Space Research Technologies Institute, Ankara (Turkey)** *(July – August 2018)*

Implemented a custom encoding algorithm for hiding n-digit number into color image in spatial domain with MATLAB and Python at Attitude and Orbit Control Department

Education

- **M.S. Middle East Technical University (METU), Ankara (Turkey)** *(Oct. 2020 – Present)*

Engineering Faculty, Computer Engineering (English)
- **B.S. Middle East Technical University (METU), Ankara (Turkey)** *(Sept. 2016 – July 2020)*

Engineering Faculty, Electrical & Electronics Engineering (English), CGPA: 3.47/4.00

Activities

- **3rd Summer Camp of APSCO Student Small Satellite Project, Shanghai (China)** *(July – August 2019)*

Attended a 3-week summer camp held at Shanghai Jiao Tong University

 - Received lectures from worldwide experts
 - Received 1U CubeSat hands-on training
 - Received Attitude Determination and Control Subsystem hands-on training
- **APSCO SSS-2B Satellite Development, TÜBİTAK UZAY Space Research Technologies Institute, Ankara (Turkey)** *(Oct. 2018 – August 2020)*

Worked as a Flight Software Development team member

Languages

English	Full Professional
Spanish	Intermediate
Turkish	Native

Skills

Python	OpenGL / GLSL	Computer Graphics
C / C++	Unity	Image Processing
VHDL	Verilog	Deep Learning
PyTorch	MATLAB	Data Mining

Hobbies & Interests

Piano	Swimming
Guitar	Video games
Volleyball	Movies